

REISSUE APPLICATION: DECLARATION BY THE INVENTOR

Docket Number (Optional)

18602-08098 (P2080R1C1)

DEC 23 2004

As a below named inventor, I hereby declare that:

My residence, mailing address and citizenship are stated below next to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is described and claimed in patent number 5,996,036, granted November 30, 1999, and for which a reissue patent is sought on the invention entitled BUS TRANSACTION REORDERING IN A COMPUTER SYSTEM HAVING UNORDERED SLAVES

the specification of which

is attached hereto.

was filed on September 22, 2003 as reissue application number 10/669,118
and was amended on September 22, 2003 via Preliminary Amendment and is a continuation of reissue application number 10/006,939, filed November 30, 2001, which was amended on November 30, 2001 via Preliminary Amendment

(If applicable)

I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to patentability as defined in 37 CFR 1.56.

I verily believe the original patent to be wholly or partly inoperative or invalid, for the reasons described below. (Check all boxes that apply.)

by reason of a defective specification or drawing.
 by reason of the patentee claiming more or less than he had the right to claim in the patent.
 by reason of other errors.

At least one error upon which reissue is based is described below. If the reissue is a broadening reissue, such must be stated with an explanation as to the nature of the broadening:

This is a broadening reissue to remove limitations in the claims as issued, by which limitations the patentee claimed less than it had the right to claim in the patent. At least one error being relied upon as the basis for reissue is the failure to claim an arbitration circuit for a computer system, the arbitration circuit adapted to couple with a plurality of slave devices having transactions queued for execution, the arbitration circuit further adapted to signal any the plurality of slave devices to reorder their transactions without signaling a microprocessor of the computer system. Another error being relied upon as the basis for reissue is the failure to claim a computer-implemented method for reordering transactions, the method comprising: receiving and queuing within a slave device a plurality of transactions for execution; and signaling the slave device to reorder its transactions without signaling a microprocessor of the computer system that the transactions are being reordered.

Docket Number (Optional)

(REISSUE APPLICATION: DECLARATION BY THE INVENTOR, page 2) 18602-08098 (P2080R1C1)

All errors corrected in this reissue application arose without any deceptive intention on the part of the applicant. As a named inventor, I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and transact all business in the United States Patent and Trademark Office connected therewith.

Names and Registration Numbers

David L. Hayes, Reg. No. 34,838; Robert R. Sachs, Reg. No. 42,120; Amir H. Raubvogel, Reg. No. 37,070;
 Brian M. Hoffman, Reg. No. 39,713; Rajiv P. Patel, Reg. No. 39,327; Laura A. Majerus, Reg. No. 33,417

Correspondence Address: Direct all communications about the application to:

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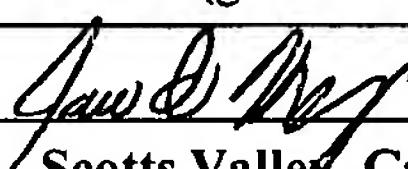


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I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine and imprisonment, or both, under 18 U.S.C. 1001, and that such willful false statements may jeopardize the validity of the application, any patent issuing thereon, or any patent to which this declaration is directed.

Full name of sole or first inventor (given name, family name) James D. Kelly

Inventor's Signature  Date 17 Dec 04

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Mailing Address 10 Torrey Oaks Court, Scotts Valley, California 95066

Full name of sole or second inventor (given name, family name) Michael L. Regal

Inventor's Signature Date

Residence Pleasanton, California Citizenship US

Mailing Address 324 Brianne Court, Pleasanton, CA 94566

Full name of sole or third inventor (given name, family name)

Inventor's Signature Date

Residence Citizenship

Mailing Address

DEC 23 2004

U.S. Patent and Trademark Office

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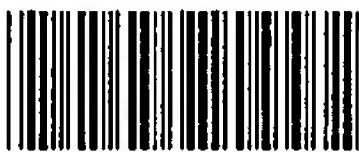
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Full name of sole or first inventor (given name, family name)		James D. Kelly		
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Full name of sole or second inventor (given name, family name)		Michael L. Regal		
Inventor's Signature		Date		
Residence	Pleasanton, California		Citizenship	US
Mailing Address	324 Brianne Court, Pleasanton, CA 94566			
Full name of sole or third inventor (given name, family name)				
Inventor's Signature		Date		
Residence			Citizenship	
Mailing Address				